



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: OZAWA, Hiroyuki et al.

Serial No.: 09/994,753

Filed: November 28, 2001

Group Art Unit: 2815

Examiner: CLARK, SHEILA V.

P.T.O. Confirmation No.: 8648

For. SEMICONDUCTOR INTEGRATED CIRCUIT WITH DUMMY PATTERNS

PETITION FOR EXTENSION OF TIME

Commissioner for Patents
Washington, D.C. 20231

Date: November 5, 2002

Sir:

Applicants petition the Commissioner for Patents to extend the time for response to the Office Action dated July 5, 2002 for one month from October 5, 2002 to November 5, 2002.

Attached please find a check in the amount of \$110.00 to cover the cost of the extension for a large entity. In the event that any additional fees are due in connection with this paper, please charge our Deposit Account No. 01-2340. Two copies of this paper are enclosed herewith.

Respectfully submitted,

ARMSTRONG, WESTERMAN & HATTORI, LLP

Thomas E. Brown
Attorney for Applicant
Reg. No. 44,450

11/05/2002 KZEWDIE 00000057 09994753

01 FC:1251

110.00 0P

TEB/kal

Atty. Docket No. 011589
Suite 1000, 1725 K Street, N.W.
Washington, D.C. 20006
(202) 659-2930



23850

PATENT TRADEMARK OFFICE

RECEIVED
NOV - 8 2002
TECHNOLOGY CENTER 2800

6p 2815
7/Ex
11/12/2
Jusla